

## i.Core MX91



i.Core MX91 is an energy-efficient SOM suitable for secure gateway, IoT, industrial devices based on the newest NXP°'s i.MX 91 processor.

The new module is equipped with Arm® Cortex®-A55 @ up to 1.4 GHz processor includes industrial features and temperature grade, a wide set of peripherals, connectivity and security options such as 2x USB 2.0.





#### **HIGHLIGHTS**

- Standard Edimm 2.0
- Powerful Arm Cortex A55 processor
- Secure gateaway, connectivity, IoT and industrial applications

#### **APPLICATIONS**















#### **FEATURES**

| СРИ      | CPU                 | NXP i.MX 91   |
|----------|---------------------|---|
|          | CORES               | Arm Cortex -A55 @ up to 1.4 GHz processor             |
|          | MEMORY              | Up to 2GB LPDDR4 @2400MTs                             |
| <b>₽</b> | OPERATING<br>SYSTEM | Linux, Android  |
| =        | POWER<br>SUPPLY     | +5V DC  |
| 77       | NETWORKING          | 2x Gb Ethernet interfaces (1x RGMII option available) |

| MASS STORAGE  Starting from 4GB eMMC drive soldered on-board  PERIPHERAL INTERFACES  UART, I <sup>2</sup> C, SPI, JTAG, CAN,SDIO, GPIOS  OPERATING TEMPERATURE*  Industrial qualified  DIMENSIONS  32 x 67,6 mm | <b>⊕</b> | USB        | <ul><li>USB OTG 2.0</li><li>USB HOST 2.0</li></ul> |
|---|----------|------------|--|
| OPERATING TEMPERATURE*  UART, I <sup>2</sup> C, SPI, JTAG, CAN,SDIO, GPIOs  Industrial qualified  | Q.       |            | Starting from 4GB eMMC drive soldered on-board     |
| TEMPERATURE* Industrial qualified   |          |            | UART, I <sup>2</sup> C, SPI, JTAG, CAN,SDIO, GPIOs |
| DIMENSIONS 32 x 67,6 mm   |          |            | Industrial qualified                               |
|   | Δ        | DIMENSIONS | 32 x 67,6 mm                                       |

<sup>\*</sup> Valid for all components except CPU. Customer shall consider junction temperature for CPU. Temperature will widely depend on application. Specific cooling solutions could be necessary for the final system.





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### **BLOCK DIAGRAM**

